# Thien LUONG

# (Garrix)

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# **SKILLS**

## **Hardware Skills:**

- Schematic, datasheets.
- Test instruments: multimeter, oscilloscope, logic analyzer.
- Hardware issue debugging.

#### **Software Skills:**

- Platform: FreeRTOS, eLinux.
- Programming: C, C++, Python.
- Testing: Unity, HW&SW integration, manufacturing test.
- Al: TDD with Cursor Al.
- · Tool: Docker, Cmake, Git, Jira.
- · Data: Minitab, Pandas, Office.
- Documentation: Confluence, Office, PlantUML.

#### Language:

English (IELTS 6.5).

## Soft skills:

- Ownership. Debugging.
- · Teamwork. Adaptability.
- · Research. Self-learning.
- Curiosity. Attention to details.

# **EDUCATION**

Ho Chi Minh City
University of Technology
2015 – 2021

BEng, PFIEV, Electrical Engineering

# **ABOUT ME**

An engineer with 4+ YOE in embedded systems, test automation, and manufacturing QA. Comfortable navigating ambiguity and building solutions from the ground up. Enjoy solving complex product problems, even when there's no clear path forward. Eager to grow in challenging environment & contribute with ownership and resilience.

# **WORK EXPERIENCES**

# R&D Center for Learning Materials, HCMUE 05/2024 - Now

## 1) Role: Firmware/Hardware Engineer (Contractor)

- Designed the hardware and firmware architecture for the STEM learning kits (ESP32, FreeRTOS).
- Collaborated with the software team to enable the data transfer between the STEM kit and PC/smartphone.

## 2) Achievements:

• MVP of a STEM kit demonstrating the Newton's 2nd law of motion.

#### **Motorola Solutions**

11/2023 - 05/2024

#### 1) Role: Embedded Software Engineer

Developed applications for CCTV (NVIDIA Orin Nano, Ubuntu).

#### 2) Achievements:

- Developed a Windows tool for DVT manufacturing phase.
- Modified device tree to control NVME SSD thermal dissipation.
- Integrated an internal cloud management tool to IP cameras.

#### **Fossil Vietnam**

08/2021 - 09/2023

# 1) Role: Embedded Manufacturing Engineer

- Developed and executed hardware test plans during NPI phases.
- Wrote drivers from scratch based on schematics and datasheets.
- Programmed and deployed Automated Test Equipment (ATE) for smartwatch testing at factory, including system setup and transfers.
- Collaborated with cross-functional teams and vendors to improve test strategies and resolve product failures.

## 2) Achievements:

- Participated in 4 projects during NPI, where I developed 5+ component drivers from scratch
- Major contributor of 4 HW test plans and their ATE systems.
- Published 10+ data analysis reports during NPI.

## **Mannheim Biomedical Engineering Lab**

03 - 12/2020

## 1) Role: Digital Signal Processing Engineer

Designed a device recording user movement, ECG during sleeping.

#### 2) Achievements:

Published 1 scientific paper in an international conference.